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Dated: September 20, 2001

Signature: 

(Robert B. Cohen)

2181
#2
Docket No.: SONYJP 3.0-159
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Nakamura et al.

Application No.: 09/840,490

Group Art Unit: 2181

Filed: April 23, 2001

Examiner: Not Yet Assigned

For: ELECTRONIC EQUIPMENT, AND METHOD
FOR CONTROLLING STATE OF PHYSICAL
LAYER CIRCUIT THEREFOR

Commissioner for Patents
Washington, DC 20231

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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

It is respectfully requested that the reference listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. The cited reference was listed on a European search report issued in connection with applicant's corresponding patent application in Europe. A copy of this reference and a copy of the European search report are enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: September 20, 2001

Respectfully submitted,

By 

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